

For Immediate Release

Press Contact:

Kernan McCoy
(805) 484-2870
kernan.mccoy@isipkg.com

Sales Contact:

Joseph Bagliere
(805) 383-8490
joe@emulation.com

Emulation Technology Now Manufactures Custom Test Fixtures for ASIC and FPGA, Package and Chip Testing

CAMARILLO, CA – April 6, 2009 – In response to design and development engineers' requests for an efficient and economical test retention system for ASIC and FPGA package and chip qualification, Emulation Technology, Inc. (ET) is now manufacturing an assortment of custom test fixtures. The ET 5100 product line was created to address the inefficiencies of medium volume applications, provide high-speed socket and interconnect solutions for single and multi-site test applications at an economical price. Applications include medium volume testing, development, debug, failure analysis and programming.

Working with the customer, ET can provide the entire fixture design including test board or the customer can provide their own test board, which ET can incorporate into the fixture. Fixtures can be designed to accept single chip packages or group of chip packages in panel form. The socket, which is included with the fixture, can operate at bandwidths up to 23 GHz with –1dB of insertion loss and pitches down to 0.1mm.

Founded in 1983, Emulation Technology, Inc., a subsidiary of Interconnect Systems, Inc., is the socket and adapter super-source for advanced interconnect and socket solutions. Emulation Technology currently designs, manufactures and markets more than 4,000 products. For further information please visit www.emulation.com

